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| 10/9 | Subclass | ISSUE CLASSIFICATION | - | | | - | PATENT NUMBER |
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| APPLICATION 09/8 | ON NO. 342734 | CONT/PRIOR | CLASS 257 | SUBCLASS | ART UNIT | EXAMINER | |
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Structure and method for fabricating semiconductor structures and devices utilizing binary metal oxide layers

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| TERMINAL | | DRAWINGS | | CLAIMS ALLOWED | | |
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| └─ DISCLAIMER | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. | |
| The term of this patent subsequent to (date) | _ | | | NOTICE OF ALLOWANCE MAILED | | |
| has been disclaimed. | (Assistant | Examiner) | (Date) | | | |
| The term of this patent shall not extend beyond the expiration date | | | | | | |
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